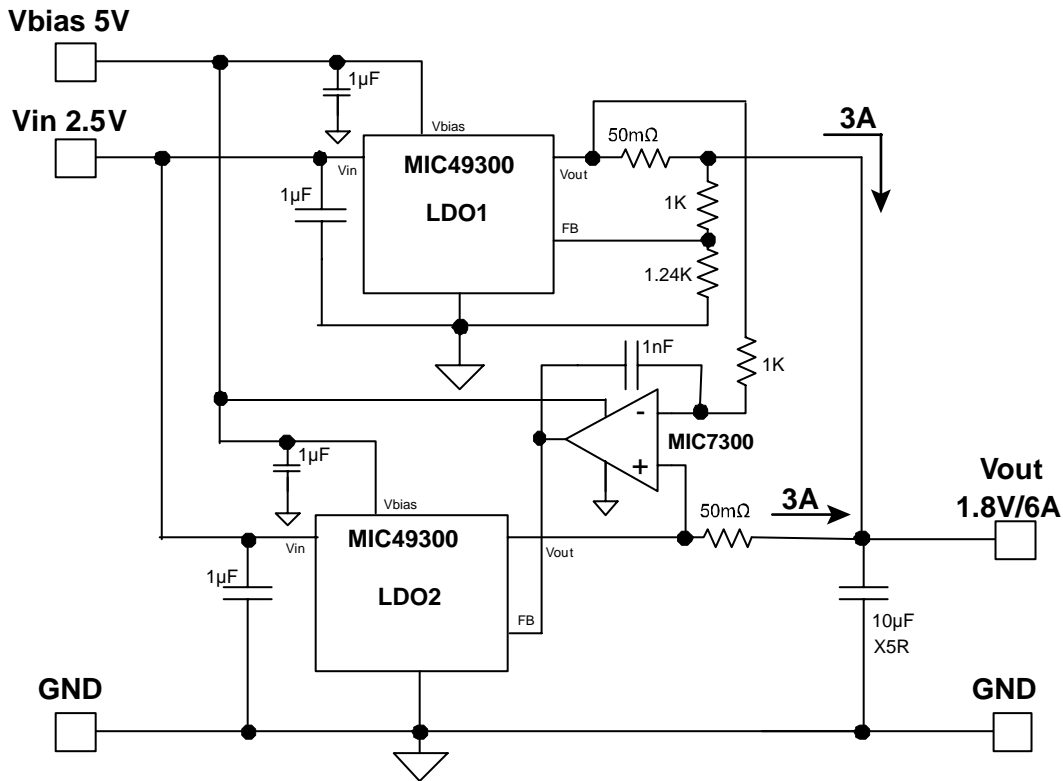


Description

This circuit allows two MIC49300 LDO regulators to share the load current equally. LDO1 senses the output voltage at the load, on the other side of a current sense resistor. As the load changes, a voltage equal to the output voltage, plus the load current times the sense resistor, and is developed at the V_{OUT} terminal of LDO 1. The Op-Amp (MIC7300) inverting pin senses this voltage and compares it to the voltage on the V_{out} terminal of LDO 2.

If the current through the current sense resistor of LDO2 is less than the current through the current sense of LDO1, the inverting pin will be at a higher voltage than the non-inverting pin and the Op-Amp will drive the FB of LDO2 low. The low voltage sensed on LDO2 FB pin will drive the output up until the output voltage of LDO2 matches the output voltage of LDO1. Since V_{OUT} will remain constant, LDO1 and LDO2 V_{OUT} terminals are matched and the sense resistances are matched, the output currents will be shared equally.



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