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KS8893ML Product Brief

Integrated 3-Port 10/100 Managed Switch-PHY

Description

The KS8893ML is a highly integrated layer-2 managed switch designed for low port count, cost-sensitive 10/100 Mbps switch systems. Its extensive feature set includes rate limiting, tag/port-based VLAN, QoS priority, MIB counters, RMII/MII/SNI and CPU control/data interfaces to effectively address both current and emerging Fast Ethernet applications.

The KS8893ML contains two 10/100 transceivers with patented mixed-signal low-power technology, three MAC (Media Access Control) units, a high-speed non-blocking switch fabric, a dedicated address lookup engine and an on-chip frame buffer memory.

Both PHY ports support 10BASE-T and 100BASE-TX with HP Auto-MDIX for reliable detection of and correction for straight-through and crossover cables. LinkMD TDR-based cable diagnostics permit identification of faulty cabling. For managed media conversion applications, one PHY port supports 100BASE-FX in addition to Fast Ethernet copper support.

QoS prioritization with four priority queues, both tag and port-based, makes the KS8893ML ideal for latency critical Voice over Internet Protocol (VoIP) phones, industrial Ethernet and triple play (voice, video, data) applications.

The KS8893ML is available in packaging, power supply and temperature options that fit the demanding needs of today's applications

Block Diagram

